

**Photovoltaic Isolator  
 Single and Dual Channel  
 5-10 Volt Output**

**General Description**

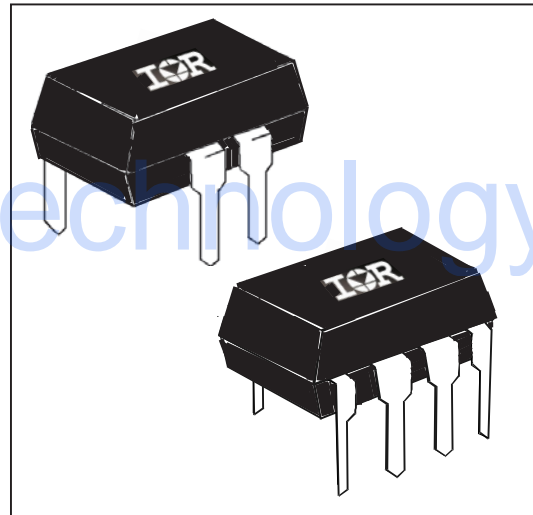
The PVI Series Photovoltaic Isolator generates an electrically isolated DC voltage upon receipt of a DC input signal. It is capable of directly driving gates of power MOSFETs or IGBTs. It utilizes a monolithic integrated circuit photovoltaic generator of novel construction as its output. The output is controlled by radiation from a GaAlAs light emitting diode (LED), which is optically isolated from the photovoltaic generator.

The PVI Series is ideally suited for applications requiring high-current and/or high-voltage switching with optical isolation between the low-level driving circuitry and high-energy or high-voltage load circuits. It can be used for directly driving gates of power MOSFETs. The dual-channel device allows its outputs to drive independent discrete power MOSFETs, or be connected in parallel or in series to provide higher current drive for power MOSFETs or higher voltage drive for IGBTs. The PVI Series Photovoltaic isolators employ fast turn-off circuitry.

These PVI Series Photovoltaic Isolators are packaged in 8-pin, molded DIP packages and available with either thru-hole or surface-mount ("gull-wing") leads, in plastic shipping tubes.

**Features**

- Isolated Voltage Source
- Monolithic Construction
- Up to 8 $\mu$ A Output
- Single or Dual Output
- Solid-State Reliability



**Applications**

- Load Distribution
- Industrial Controls
- Current-to-Voltage Conversion
- Custom Solid-State Relay

**Part Identification**

|                |                              |
|----------------|------------------------------|
| PVI1050NPbF    |                              |
| PVI5050NPbF    | thru-hole                    |
| PVI5080NPbF    |                              |
| PVI1050NSPbF   | surface-mount                |
| PVI5050NSPbF   | (gull-wing)                  |
| PVI5080NSPbF   |                              |
| PVI1050NS-TPbF | surface-mount, tape and reel |

**Electrical Specifications** ( $-40^{\circ}\text{C} \leq T_A \leq +85^{\circ}\text{C}$  unless otherwise specified)

| INPUT CHARACTERISTICS                                    | PVI Series | Units              |
|--|------------|--------------------|
| Input Current Range (see figure 4)                       | 2.0 to 50  | mA (DC)            |
| Maximum Forward Voltage Drop @ 10mA, 25°C (see figure 5) | 1.4        | V (DC)             |
| Maximum Reverse Voltage                                  | 6.0        | V(DC)              |
| Maximum Reverse Current @ -6.0V (DC), 25°C               | 100        | $\mu\text{A}$ (DC) |
| Maximum Pulsed Input Current @ 25°C (see figure 6)       | 1.0        | A(peak)            |

| OUTPUT CHARACTERISTICS                     | PVI Series      | Units              |
|--|-----------------|--------------------|
| Maximum Forward Voltage @ 10 $\mu\text{A}$ | 8.0 per channel | V <sub>(DC)</sub>  |
| Maximum Reverse Current @ -10VDC           | 10              | $\mu\text{A}$ (DC) |

| COUPLED CHARACTERISTICS  | PVI5050N | PVI5080N | PVI1050N                    | Units              |
|--|----------|----------|-----------------------------|--------------------|
| Minimum Open Circuit Voltage @ ILED = 10mA, 25°C, RL = >10M $\Omega$<br>(see figures 1 to 2) | 5.0      |          | 5.0/channel<br>10 series    | V (DC)             |
| Minimum Short Circuit Current @ ILED = 10mA, 25°C (see figures 1 to 2)                       | 5.0      | 8.0      | 5.0 /channel<br>10 parallel | $\mu\text{A}$ (DC) |
| Maximum Capacitance (Input/Output)   | 1.0      |          | 2.0                         | pF                 |
| Maximum Ton Time @ ILED=10mA, CLOAD=10pF (See Figure7)                                       |          |          |                             |                    |
| RL>20M $\Omega$  | 300      |          |                             | $\mu\text{S}$      |
| RL=10M $\Omega$  | 160      |          |                             | $\mu\text{S}$      |
| RL=4.7M $\Omega$   | 90       |          |                             | $\mu\text{S}$      |
| Maximum Toff Time @ ILED=10mA, CLOAD=10pF (See Figure7)                                      | 220      |          |                             | $\mu\text{S}$      |

| GENERAL CHARACTERISTICS  | PVI5050N/5080N   | PVI1050N | Units            |
|--|------------------|----------|------------------|
| Min. Dielectric Strength, Input-Output   | 4000             | 2500     | V <sub>RMS</sub> |
| Min. Dielectric Strength, Output-to-Output   | 1200             |          | V <sub>DC</sub>  |
| Min. Insulation Resistance, Input-to-Output<br>@T <sub>A</sub> =+25°C, 50%RH, 100V <sub>DC</sub> | 10 <sup>12</sup> |          | $\Omega$         |
| Max. Pin Soldering Temperature (10 seconds max.)   | +260             |          | °C               |
| Ambient Temperature Range: Operating   | -40 to +85       |          | °C               |
| Storage  | -40 to +125      |          | °C               |

International Rectifier does not recommend the use of this product in aerospace, avionics, military or life support applications. Users of this International Rectifier product in such applications assume all risks of such use and indemnify International Rectifier against all damages resulting from such use.

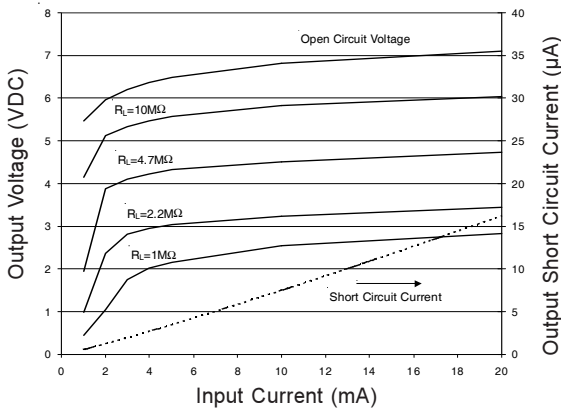


Figure 1. PVI5050N, PVI1050N Typical Output Characteristics

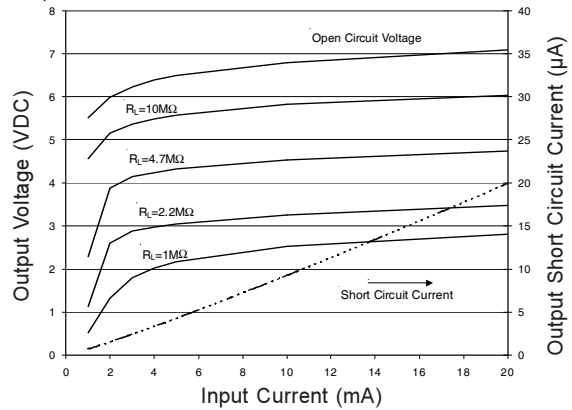


Figure 2. PVI5080N Typical Output Characteristics

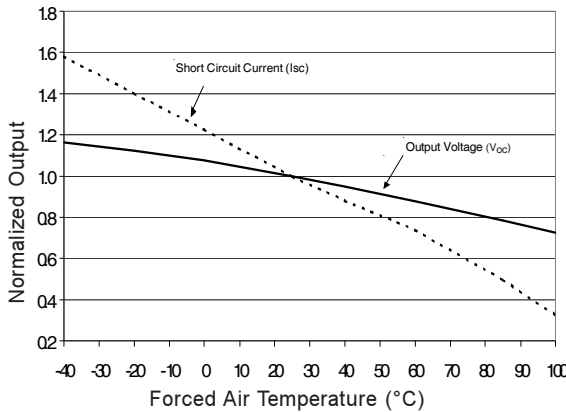


Figure 3. Typical Variation of Output

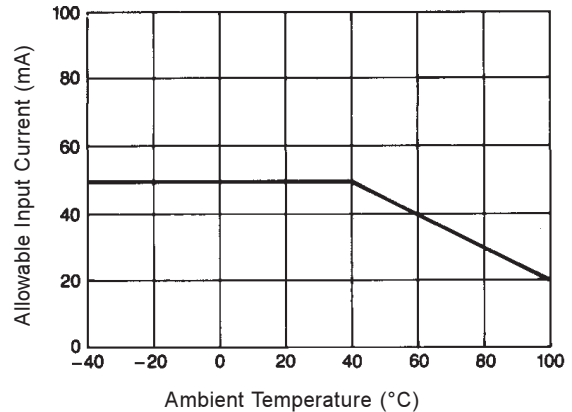


Figure 4. Input Current Derating

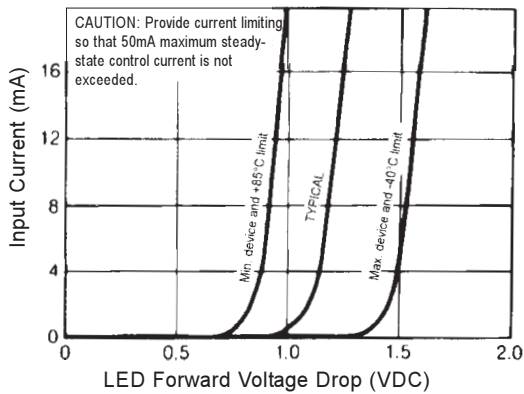


Figure 5. Input Characteristics

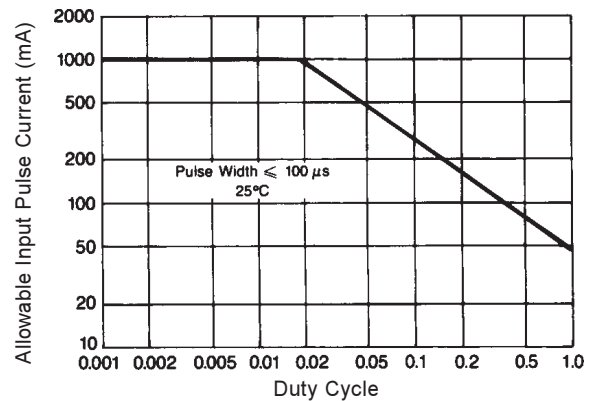


Figure 6. Input Pulse Capability

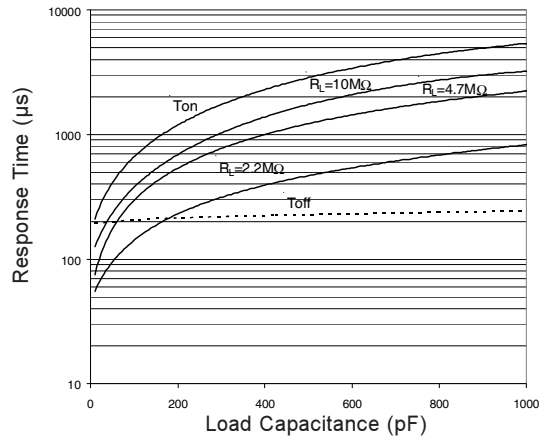
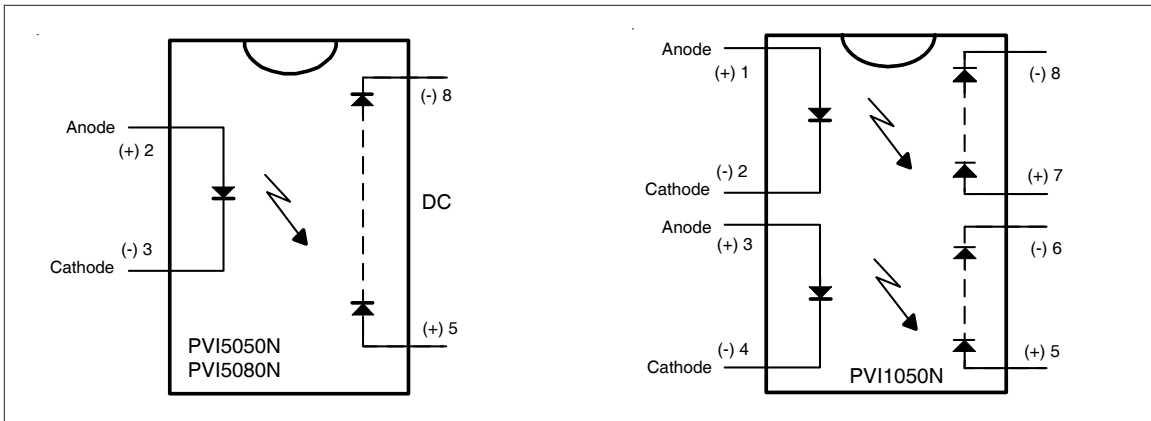


Figure 7. Typical Response Time

### Wiring Diagram

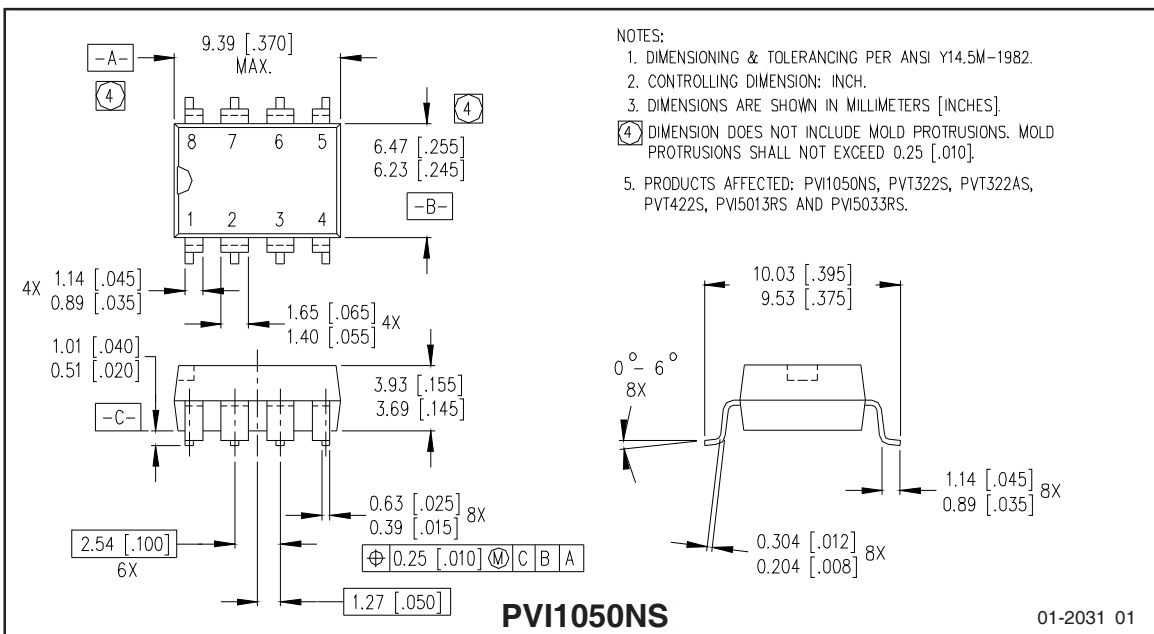
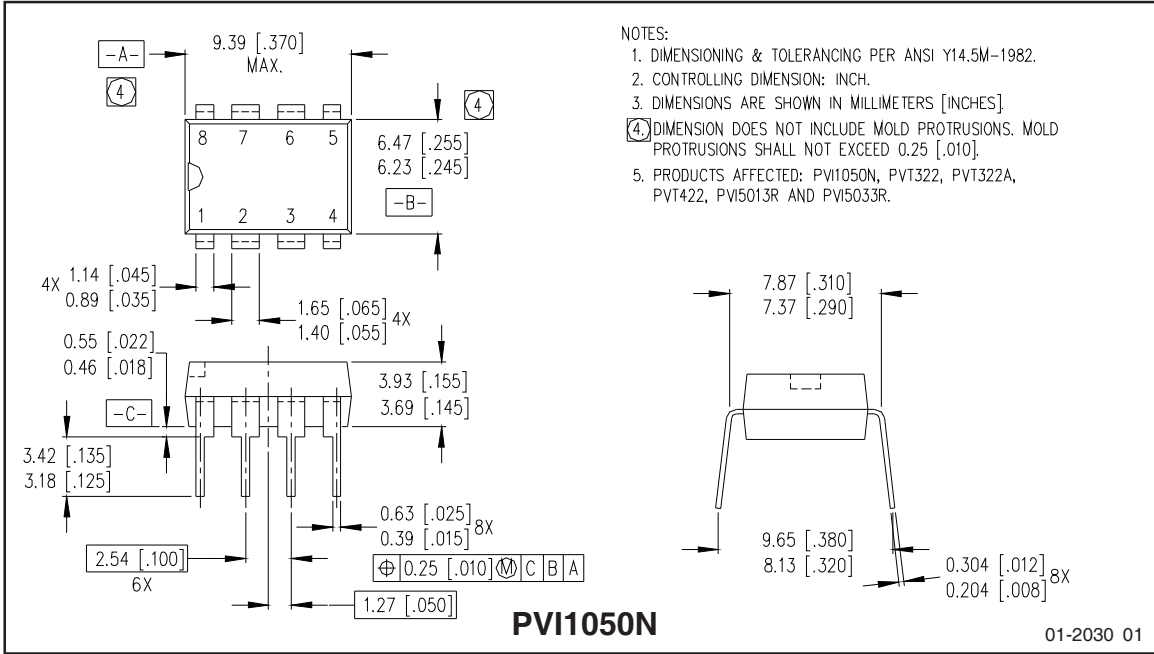


### Application Note:

The outputs of the PVI1050N (pins 5-6 and 7-8) may be placed in series connection to produce a 10-volt output with a 5µA minimum short circuit current. Alternatively, the two outputs of the PVI1050 may be connected in parallel to produce a 5.0-volt output with a 10µA minimum short circuit current.

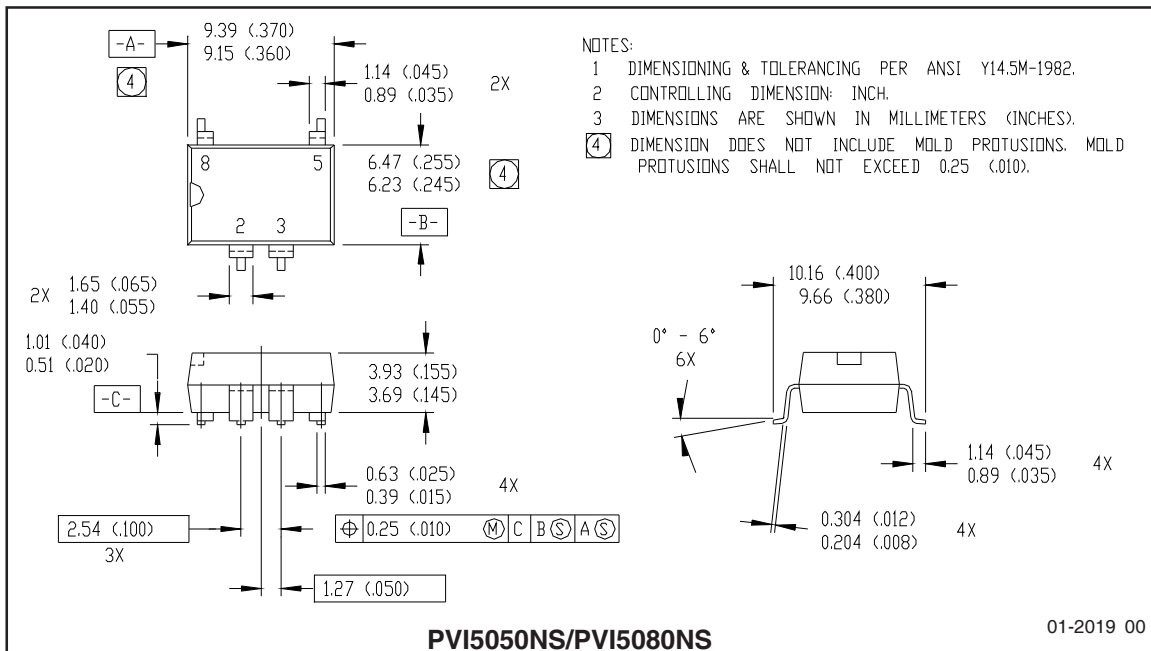
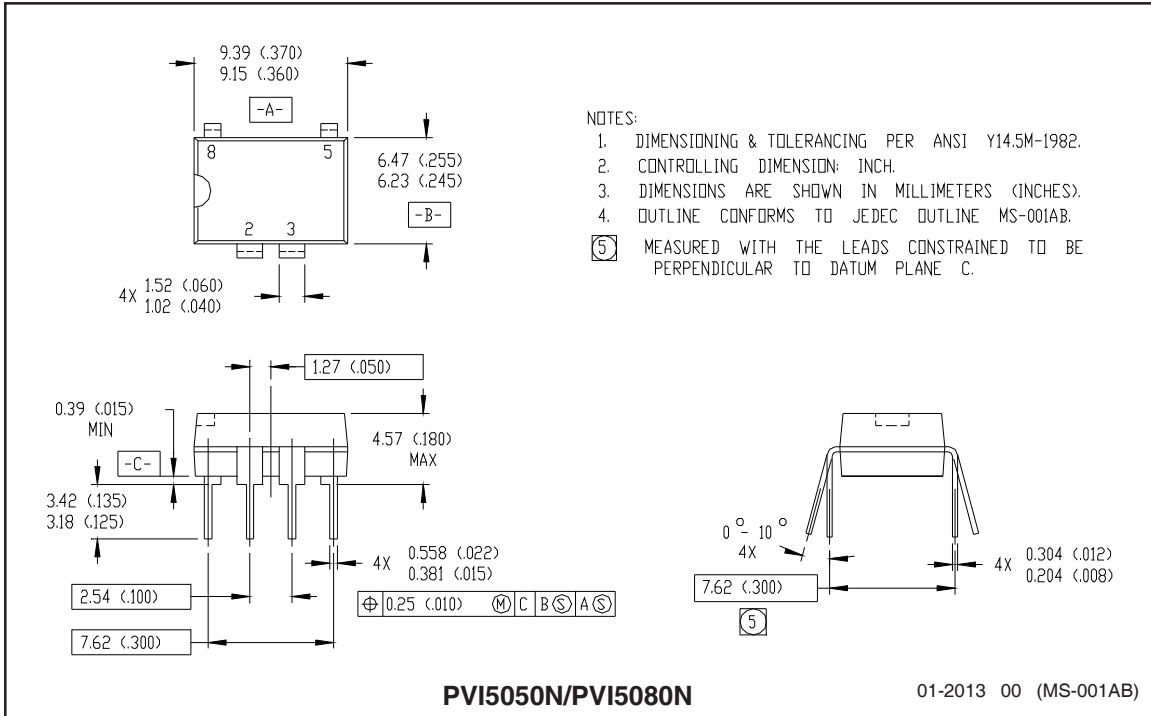
The two outputs of the PVI1050N may be applied separately with a maximum 1200VDC between the outputs. Input-to-output isolation to either output is 2500V (RMS).

## Case Outlines



Note: For the most current drawing please refer to IR website at: <http://www.irf.com/package/>

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**Qualification information<sup>†</sup>**

| Qualification level        | Indusrid<br>(per JEDEC JESD47 <sup>††</sup> guidelines) |  |
|----------------------------|---|--|
| Moisture Sensitivity Level | PVI1050NPbF   | N/A  |
|                            | PVI5050NPbF   |  |
|                            | PVI5080NPbF   |  |
|                            | PVI1050NSPbF  | MSL4<br>(per JEDEC J-STD-020E & JEDEC J-STD-033C <sup>††</sup> ) |
|                            | PVI5050NSPbF  |  |
|                            | PVI5080NSPbF  |  |
|                            | PVI1050NS-TPbF  |  |
| RoHS compliant             | Yes   |  |

† Qualification standards can be found at International Rectifier's web site  
<http://www.irf.com/product-info/reliability>

†† Applicable version of JEDEC standard at the time of product release.

**Revision History**

| Date      | Comment   |
|-----------|---|
| 4/24/2015 | <ul style="list-style-type: none"> <li>Added Qualification Information table on page 7.</li> <li>Updated data sheet based on corporate template.</li> </ul> |

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